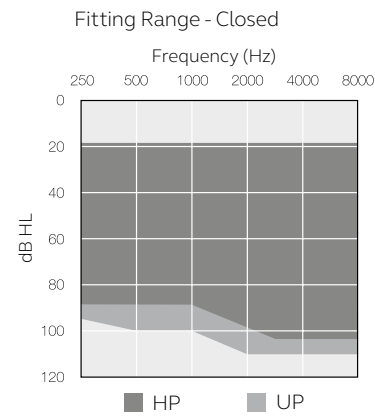
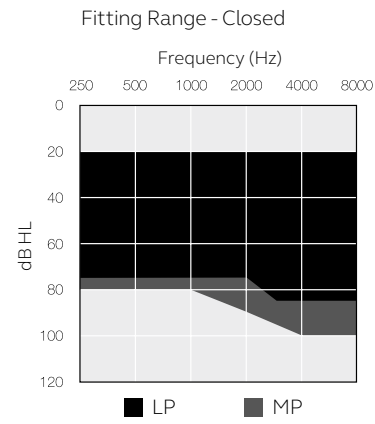


# danalogic Ambio Smart



Model	AM7CIC-W
<b>Device Configurations</b>	
Battery	10A
Power Levels	LP, MP, HP & UP
<b>Audiological Features</b>	
WARP compression (WDRC) - number of channels	17
Binaural Environmental Optimizer II	●
Environmental Optimizer	-
Noise Tracker II	●
Expansion	●
Impulse Noise Reduction	●
Sound Shaper	●
DFS Ultra II	●
Music Mode	●
Synchronized Acceptance Manager	●
Low Frequency Boost (Only UP)	●
Amplification Strategy (WDRC/Semi-Linear/Linear - Only UP)	●
Tinnitus Sound Generator	●
<b>Functional Features</b>	
Synchronized Push Button *	●
Smart Start	●
Phone Now	●
Comfort Phone	●
Ear to Ear Communication	●
Direct audio streaming (Made for Apple)	●
TV Streamer 2, Remote Control 2, Phone Clip+, Micro Mic and Multi Mic	●
BeMore app	●
<b>Online Services</b>	
Remote Fine Tuning	●
Remote Firmware Updates	●
Live Assist	●
<b>Fitting Features</b>	
Danalogic 1.10 or higher	●
Fully Flexible Programs	4
Auto DFS	●
Onboard Analyzer II	●
Noahlink Wireless	●
* Also including functionality for synchronized Push Button Volume Control	



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# Technical Specifications

		LP		MP		
		IEC 60118-0:1983_AMD1:1994 IEC 60118-0:2015 IEC 711 Ear simulator	ANSI S3.22-2014 IEC 60118-0:2015 JIS C 5512: 2015 2cc coupler	IEC 60118-0:1983_AMD1:1994 IEC 60118-0:2015 IEC 711 Ear simulator	ANSI S3.22-2014 IEC 60118-0:2015 JIS C 5512: 2015 2cc coupler	
Reference test gain (60 dB SPL input)	1600 Hz/HFA	33	32	40	37	dB
Full-on gain (50 dB SPL input)	Max. 1600 Hz/HFA	49 43	40 37	59 51	50 45	dB
Maximum output (90 dB SPL input)	Max. 1600 Hz/HFA	124 117	114 109	128 121	118 114	dB SPL
Total harmonic distortion	500 Hz 800 Hz 1600 Hz 3200 Hz	0.5 0.5 0.5 -	0.4 0.5 0.7 0.1	0.7 1.1 0.8 -	0.8 0.9 1.0 0.3	%
Equivalent input noise, w/o Noise reduction		22	22	25	24	dB SPL
1/3 Octave Equivalent input noise, w/o Noise reduction	1600 Hz	10	10	11	11	dB SPL
Frequency range IEC 60118-0: 2015		100-9500	100-9020	100-9210	100-8170	Hz
Current Drain (Quiescent / Operating)		1.12/1.14	1.12/1.22	1.10/1.13	1.10/1.30	mA

Data in accordance with IEC60118-0 Edition 3.0 2015-06, IEC60118-7 and ANSI S3.22-2009, supply voltage 1.3V

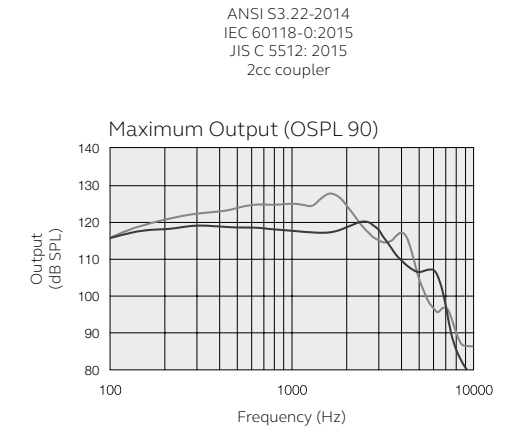
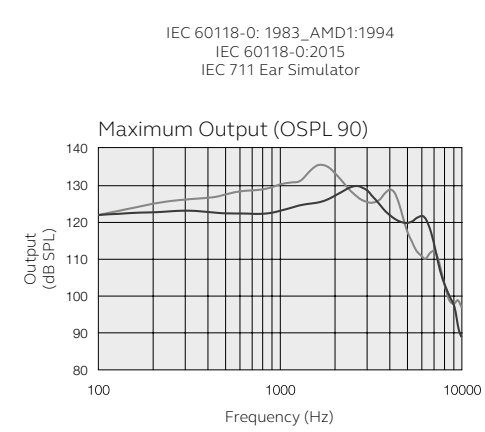
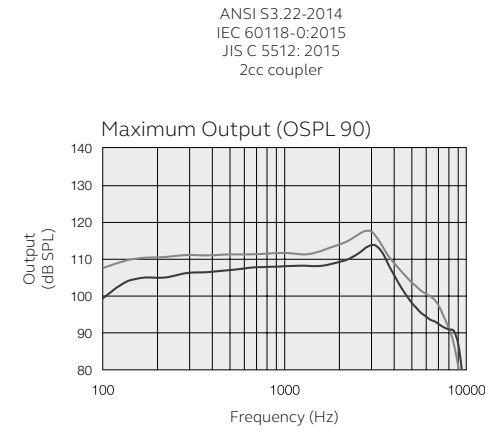
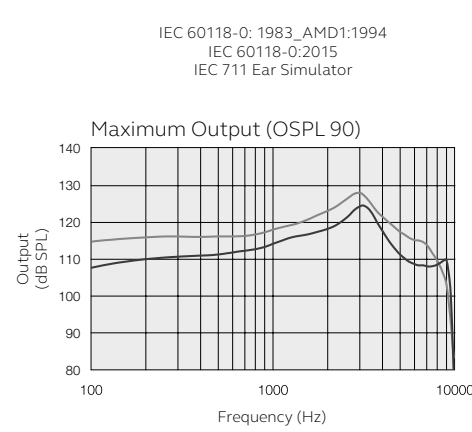
# Technical Specifications

		HP		UP		
		IEC 60118-0:1983_AMD1:1994 IEC 60118-0:2015 IEC 711 Ear simulator	ANSI S3.22-2014 IEC 60118-0:2015 JIS C 5512: 2015 2cc coupler	IEC 60118-0:1983_AMD1:1994 IEC 60118-0:2015 IEC 711 Ear simulator	ANSI S3.22-2014 IEC 60118-0:2015 JIS C 5512: 2015 2cc coupler	
Reference test gain (60 dB SPL input)	1600 Hz/HFA	47	42	58	47	dB
Full-on gain (50 dB SPL input)	Max. 1600 Hz/HFA	69 58	60 53	76 71	68 62	dB
Maximum output (90 dB SPL input)	Max. 1600 Hz/HFA	130 125	120 118	135 135	128 124	dB SPL
Total harmonic distortion	500 Hz 800 Hz 1600 Hz 3200 Hz	0.5 1.0 0.8 -	0.4 0.8 0.3 0.2	0.6 1.1 0.2 0.1	0.6 0.7 0.2 0.1	%
Equivalent input noise, w/o Noise reduction		25	23	19	21	dB SPL
1/3 Octave Equivalent input noise, w/o Noise reduction	1600 Hz	11	11	12	12	dB SPL
Frequency range IEC 60118-0: 2015		100-7370	100-6790	100-7180	100-4820	Hz
Current Drain (Quiescent / Operating)		1.17/1.20	1.17/1.24	1.11/1.17	1.11/1.16	mA

Data in accordance with IEC60118-0 Edition 3.0 2015-06, IEC60118-7 and ANSI S3.22-2009, supply voltage 1.3V

Patents pending

All specifications are subject to change without notice



Patents pending

All specifications are subject to change without notice

